



Docket No.: M4065.0767/P767
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Barmak Mansoorian

Allowed: May 26, 2006

Application No.: 09/359,056

Confirmation No.: 2286

Filed: July 21, 1999

Art Unit: 2615

For: HIGH RESOLUTION CMOS CIRCUIT
USING A MATCHED IMPEDANCE
OUTPUT TRANSMISSION LINE

Examiner: N. T. Tran

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicant has reviewed the Examiner's Statement of Reasons for Allowance and wishes to submit the following remarks. The statement paraphrases several limitations from independent claims 1, 8, 11, 14, 15 and 17, and states that the prior art does not disclose or suggest such limitations in combination with the remaining limitations of the claim. Applicant wishes to add that dependent claims 3, 4, 6, 9, 10, 12, 13 and 16 recite limitations which, in combination with their base claims, render the dependent claims allowable.

Otherwise, Applicant agrees with the Examiner that the allowed claims distinguish over the prior art.

Dated: August 21, 2006

Respectfully submitted,

By 

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